ABSTRACT

[0061] A method and apparatus that coordinates refresh and parity-scanning in DRAM-based devices such that parity-scan operations substitute for refresh operations when both operations are required in the system. The process of parity-scanning automatically refreshes the entries being scanned, subject to refresh and parity-scan interval requirements. As such, refresh and parity-scan operations may be performed in a single operation, which bolsters the scheduling and performance of the two operations.